

0 16 22 26 30 34 38 42 46 50 54 58 62 66 70 74 78 82 86 90 94 98

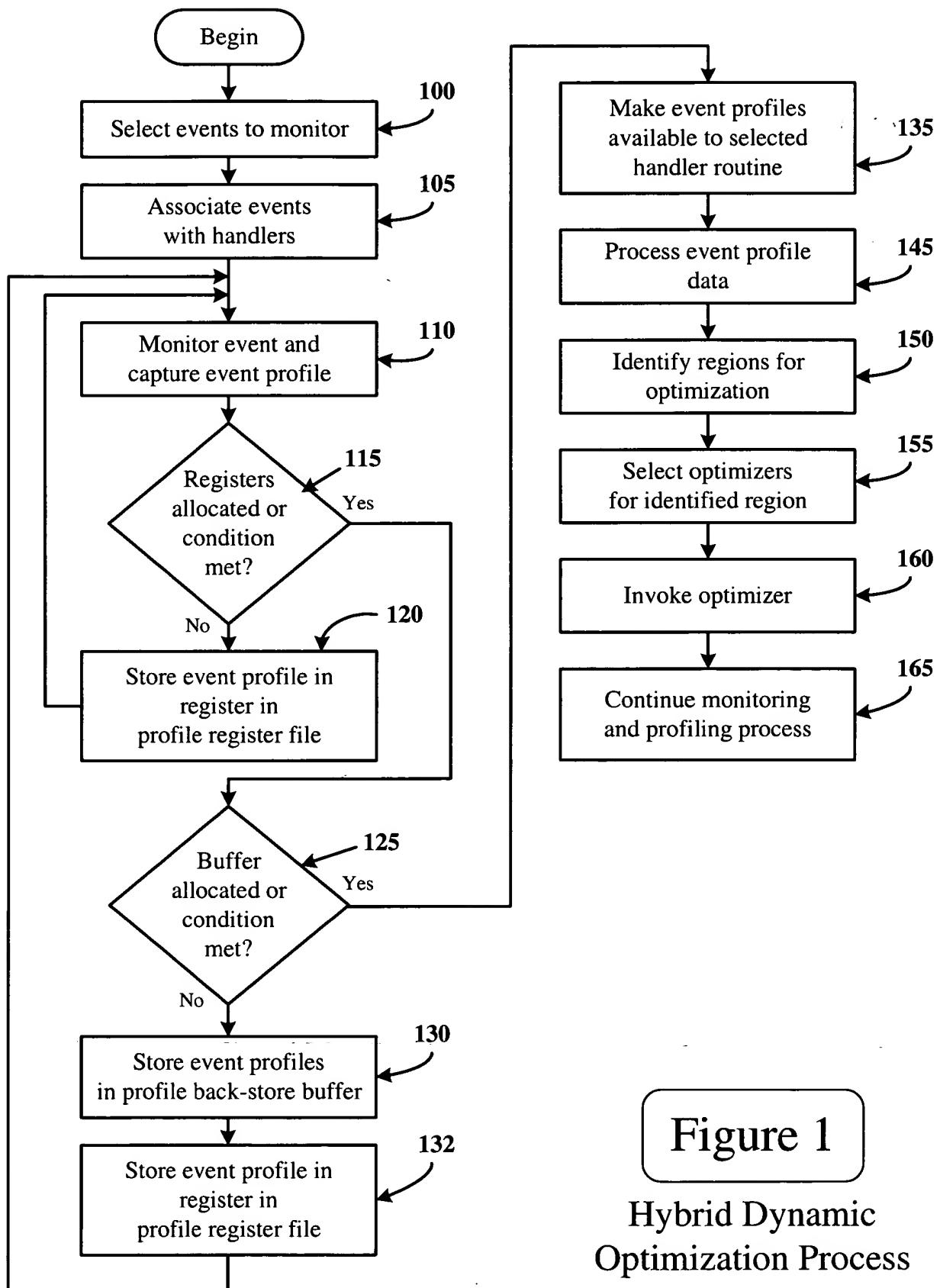


Figure 1
Hybrid Dynamic Optimization Process

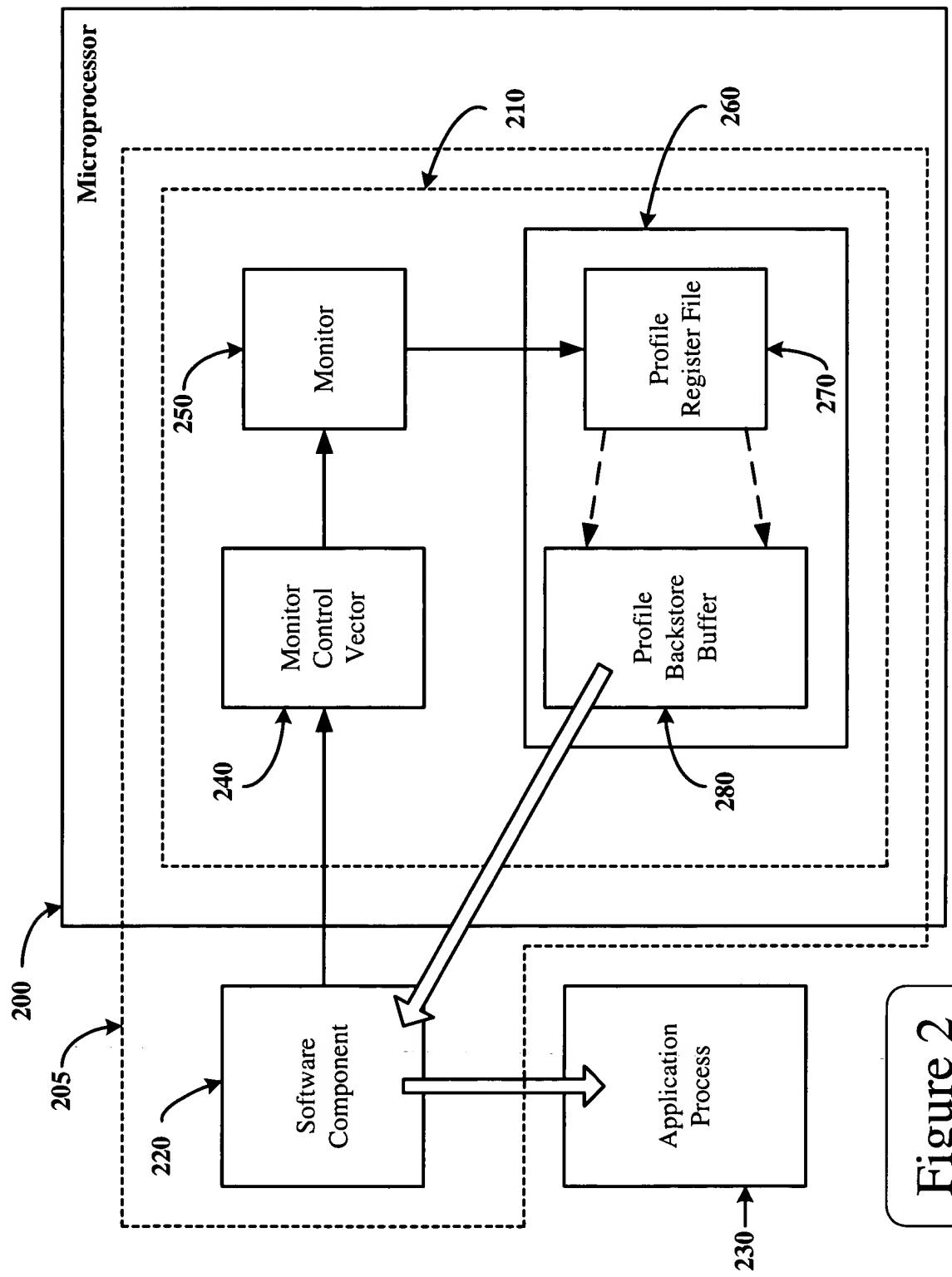
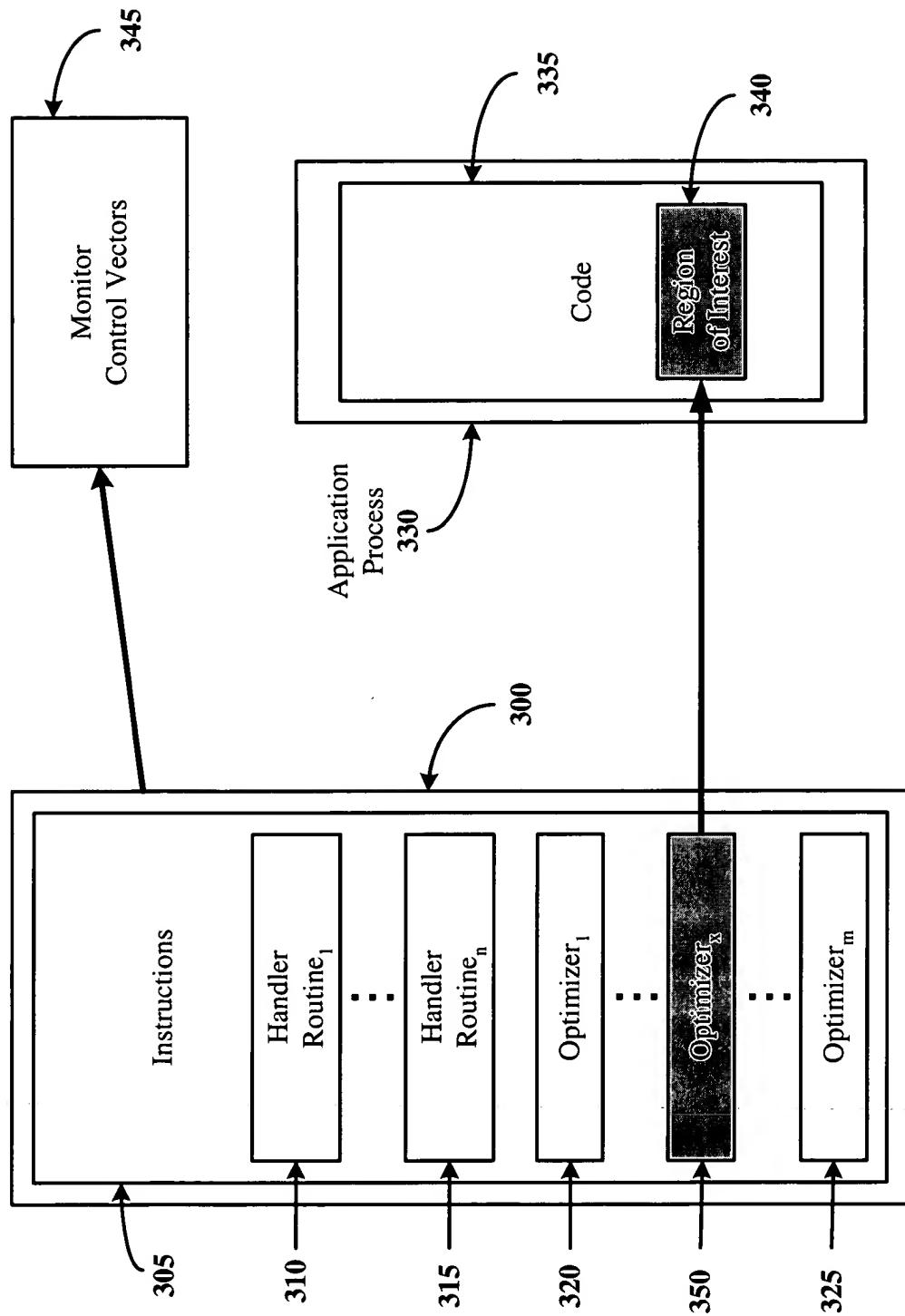


Figure 2

Hybrid Dynamic Optimizer



Software Component

Figure 3

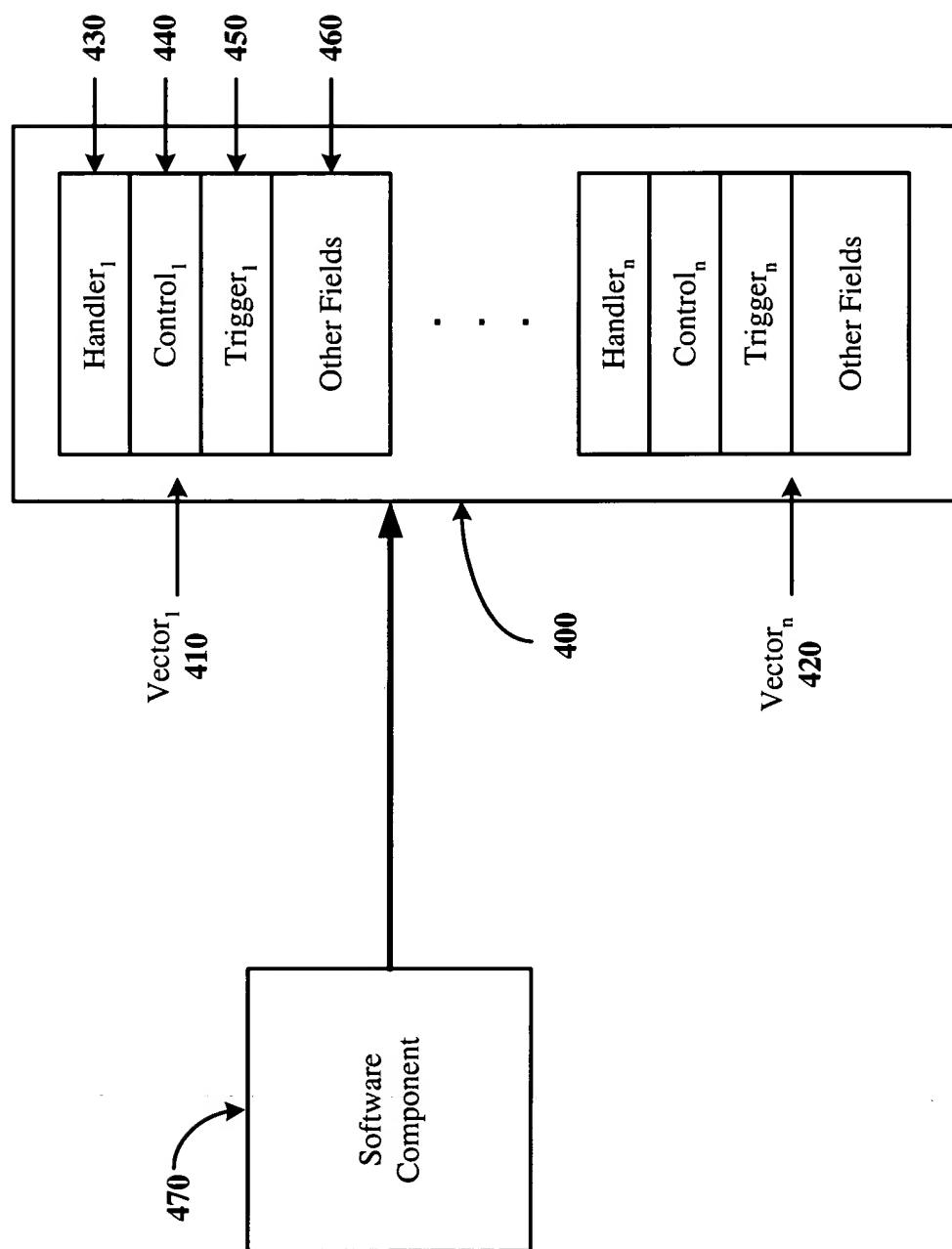


Figure 4

Monitor Control Vectors

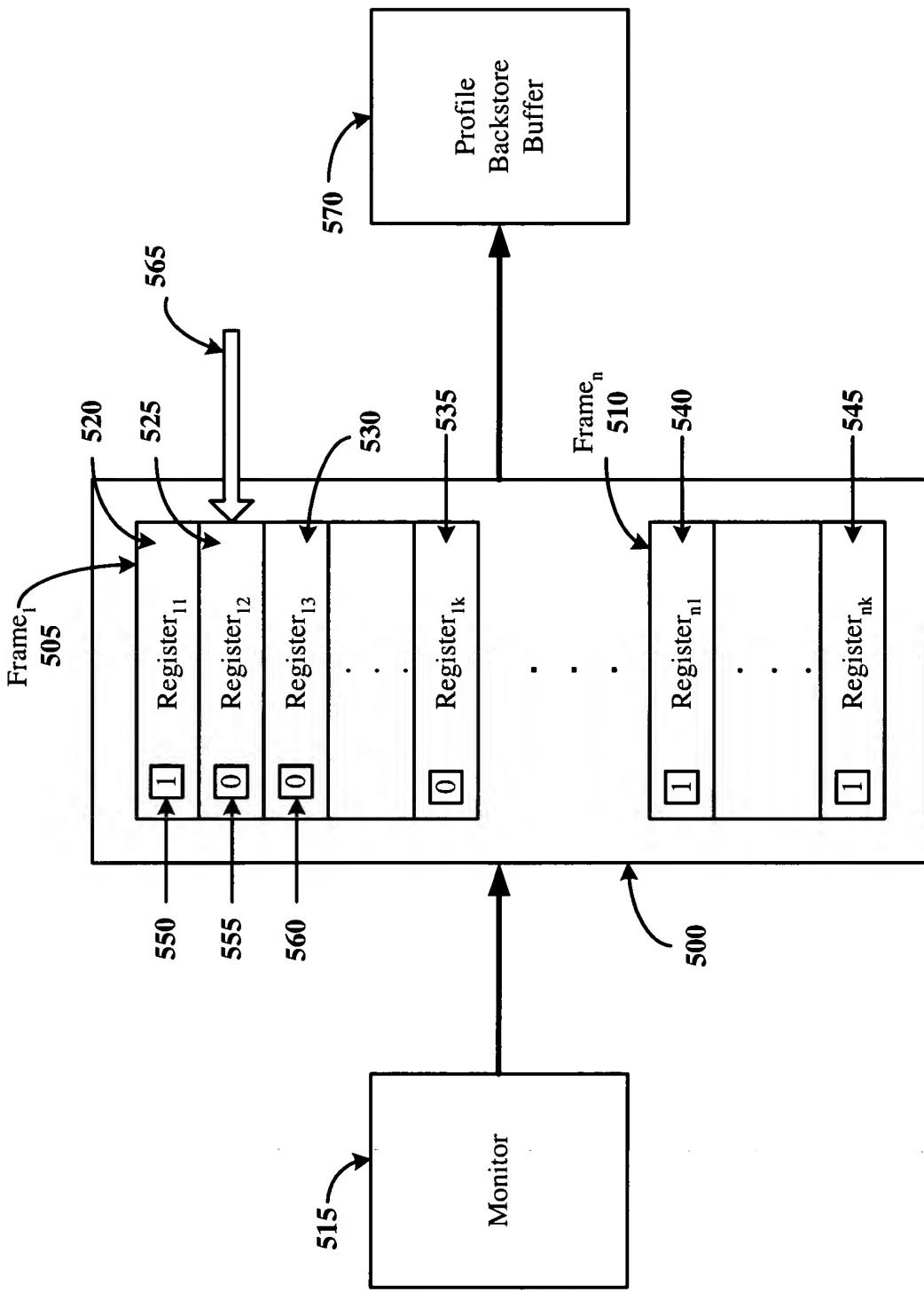


Figure 5

Profile Register File

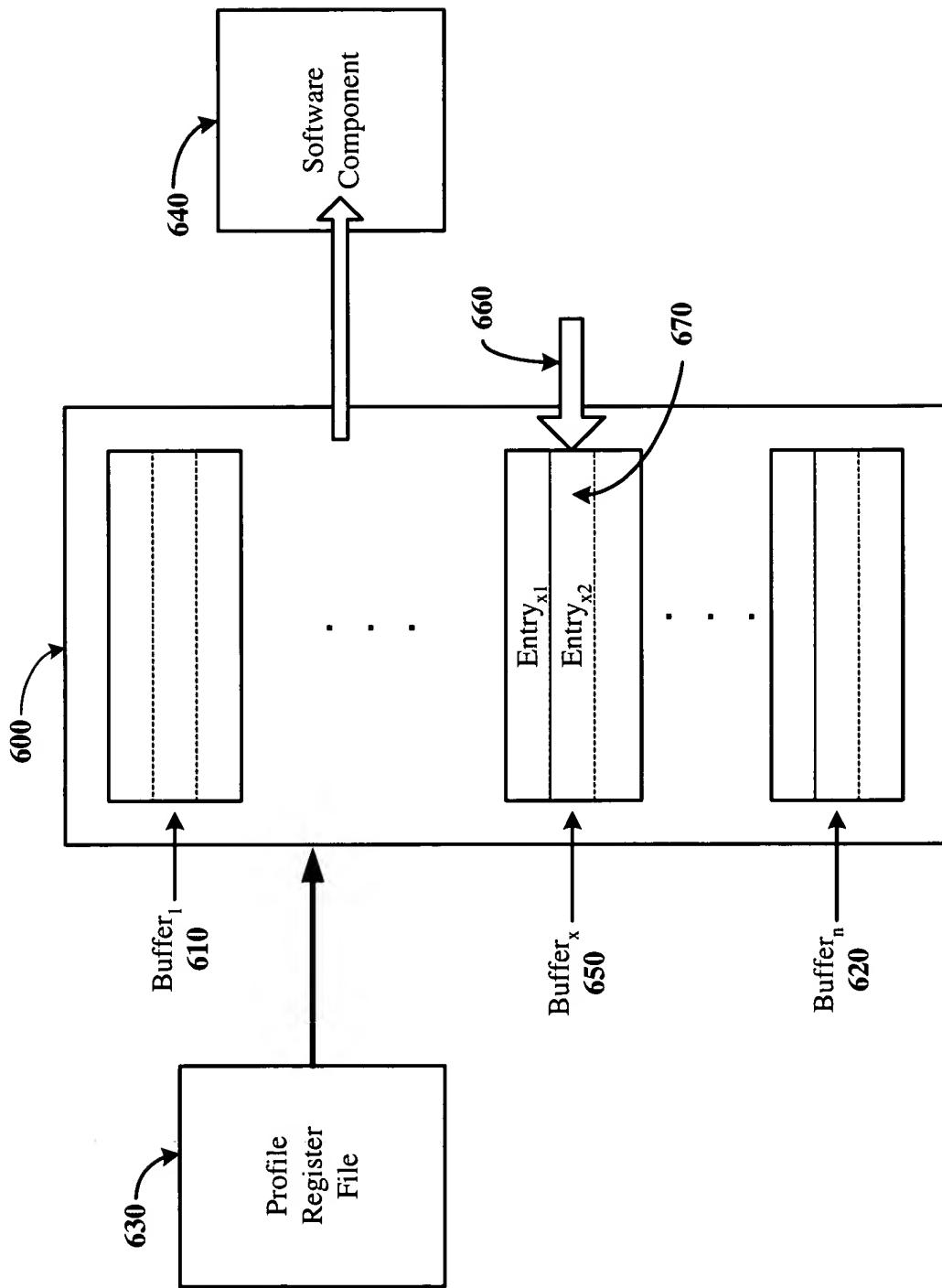


Figure 6

Profile Backstore Buffer

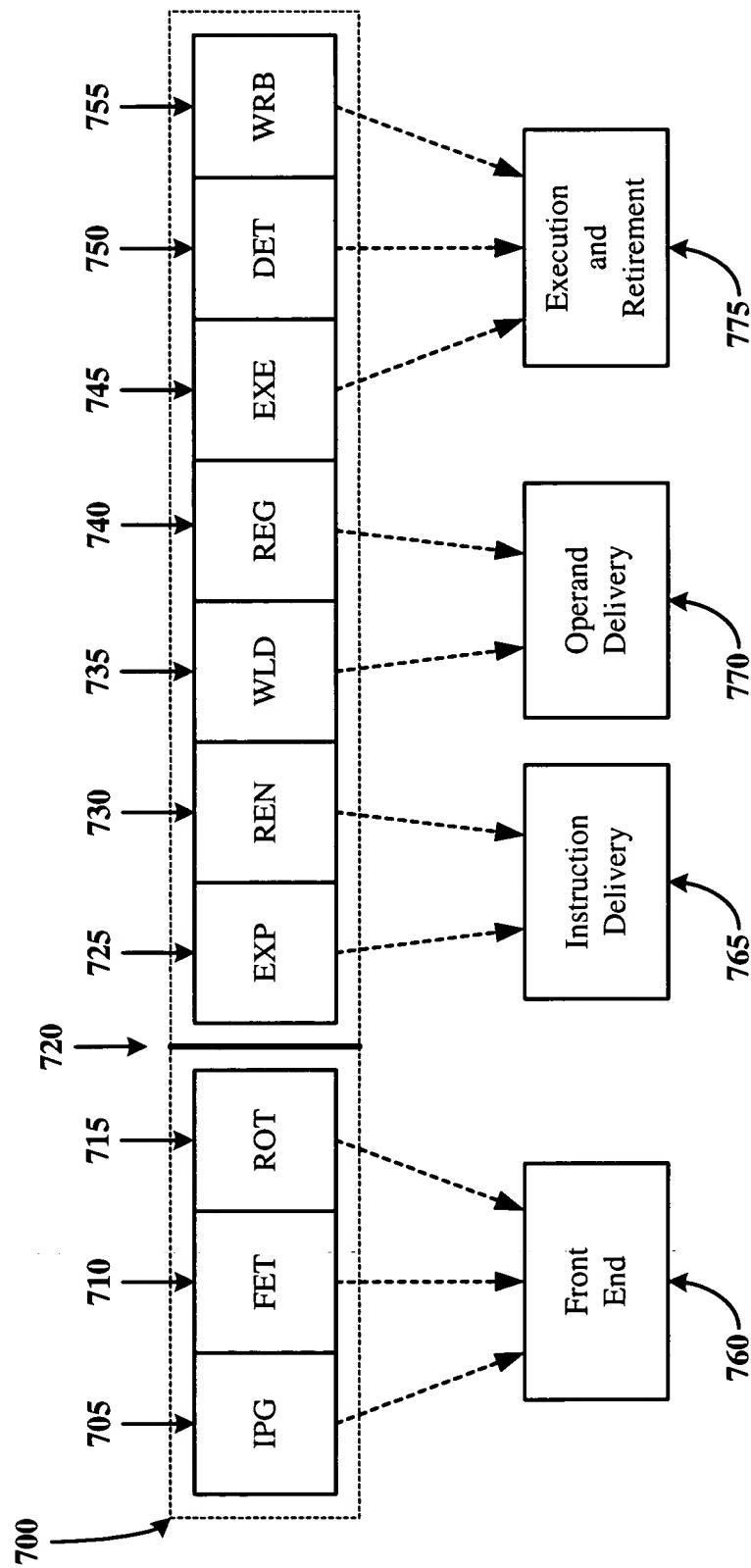


Figure 7

Microprocessor Pipeline